

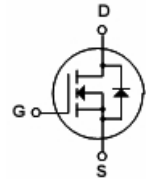
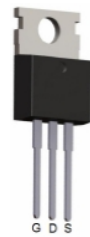
TS5N60

600V N-Channel Mosfet

DRAWING

Features

- ◆ 5.0A,600V, $R_{DS(ON)}=2.5\Omega@V_{GS}=10V$
- ◆ Low gate charge(typical 16nc)
- ◆ Low crss (typical 9.0pF)
- ◆ Fast switching
- ◆ 100%avalanche tested
- ◆ Improved dv/dt capability



General Description

- ◆ Package:TO-220C
- ◆ This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the Avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply.

Absolute Maximum Ratings

Symbol	Parameter	Spec	Units
V_{DSS}	Drain-Source Voltage	600	V
I_{AR}	Avalanche Current	5	A
I_D	Drain Current -Continuous($T_c=25^\circ C$)	5	A
I_{DM}	Drain Current -Pulsed	20	A
V_{GSS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy	210	mJ
E_{AR}	Repetitive Avalanche Energy	10	mJ
dv/dt	Peak Diode Recovery dv/dt	4.5	V/ns
P_D	Power Dissipation ($T_C=25^\circ C$)	100	W
T_j	Junction Temperature	+150	$^\circ C$
T_{opr}	Operating Temperature Range	-55 to +150	$^\circ C$
T_{stg}	Storage Temperature	-55 to +150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typ	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	—	1.25	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	—	62.5	$^\circ C/W$

Electrical Characteristics(Tc=25°C unless otherwise noted)
Off Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	600	—	—	V
BV _{DSS} /T _J	Breakdown Voltage Temperature Coefficient	ID=250uA, Referenced to 25°C	—	0.6	—	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =600V, V _{GS} =0V	—	—	10	uA
		V _{DS} =480V, T _c =125°C	—	—	100	uA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} =30V, V _{DS} =0V	—	—	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} =-30V, V _{DS} =0V	—	—	-100	nA

On Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
V _{GSTH}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	—	4	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =2.5A	—	2.1	2.5	Ω
G _{fs}	Forward Transconductance	V _{DS} =15, I _D =2.25A	—	4	—	S

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
C _{JSS}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	—	560	730	pF
C _{OSS}	Output Capacitance		—	80	100	pF
C _{rss}	Reverse Transfer Capacitance		—	9	12	pF

Switching Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
t _{don}	Turn-On Delay Time	V _{DD} =300V I _D =5.0A R _G =25Ω	—	13	35	ns
t _r	Turn-On Rise Time		—	45	100	ns
t _{doff}	Turn-Off Delay Time		—	35	80	ns
t _f	Turn-Off Fall Time		—	40	90	ns
Q _g	Total Gate Charge	V _{DS} =480V	—	16	20	nc
Q _{gs}	Gate-Source Charge	I _D =5.0A	—	3.5	—	nc
Q _{gd}	Gate-Drain Charge	V _{GS} =10V	—	7.8	—	nc

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I _s	Maximum Continuous Drain-source diode forward current		—	—	5	A
I _{sm}	Maximum pulsed drain-source diode diode forward current		—	—	20	A
V _{sd}	Drain-source diode forward Voltage	V _{GS} =0V, I _S =5.0A	—	—	1.5	V
T _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =5.0A	—	270	—	ns
Q _{rr}	Reverse Recovery charge	dif/dt=100A/us	—	1.9	—	uc

Typical Characteristics

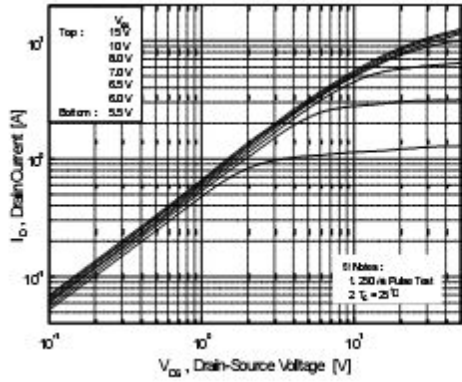


Figure1. On-Region Characteristics

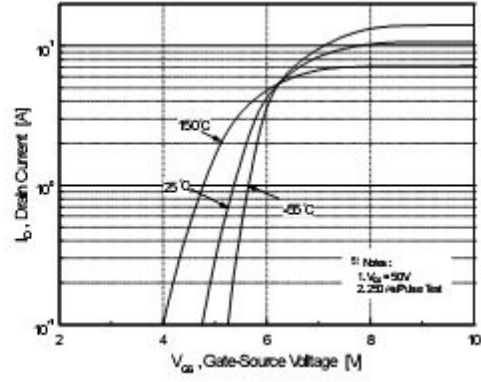


Figure2. Transfer Characteristics

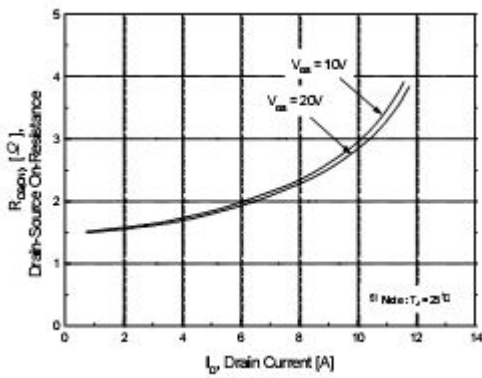


Figure3. On-Resistance Variation vs Drain Current and Gate Voltage

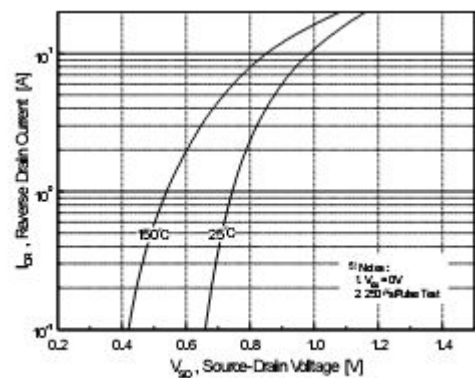


Figure4. Body Diode Forward Voltage Variation with Source Current and Temperature

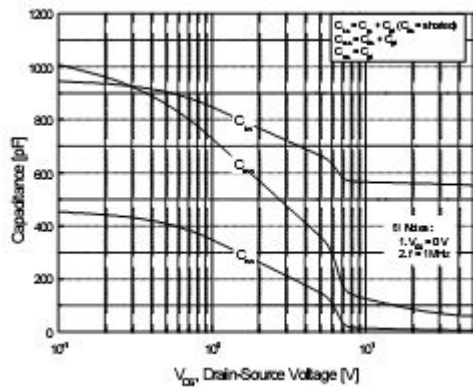


Figure5. Capacitance Characteristics

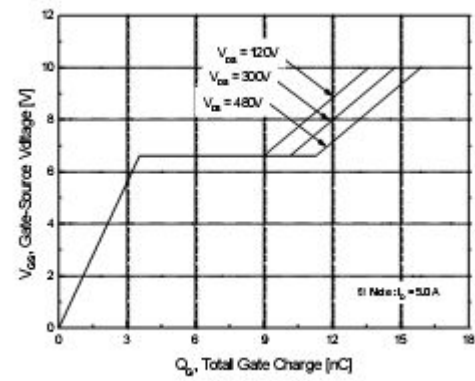


Figure6. Gate Charge Characteristics

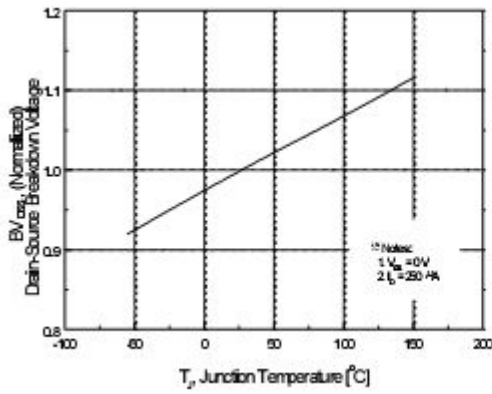


Figure7. Breakdown Voltage Variation vs Temperature

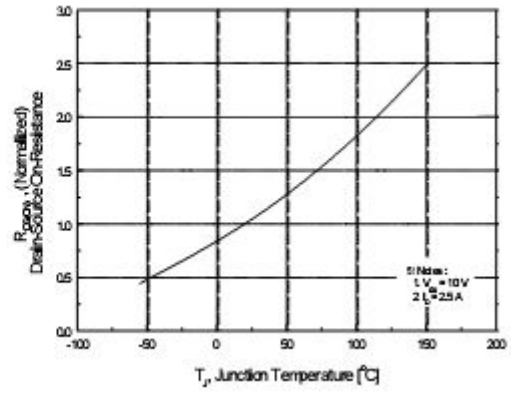


Figure8. On-Resistance Variation vs Temperature

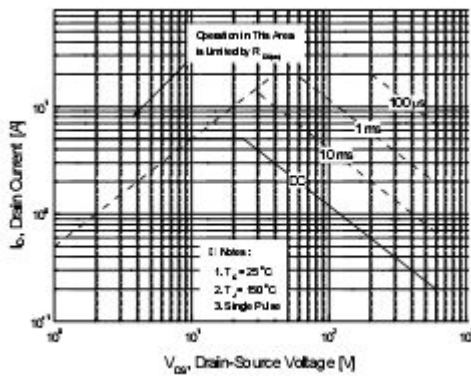


Figure9. Maximum Safe Operating Area

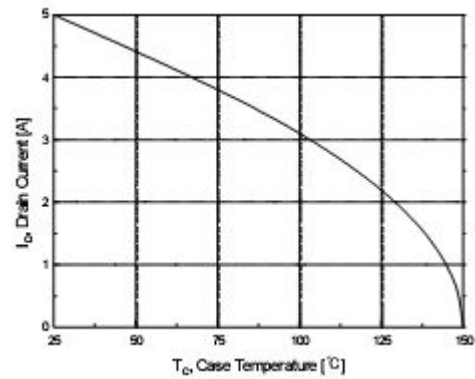


Figure10. Maximum Drain Current vs Case Temperature

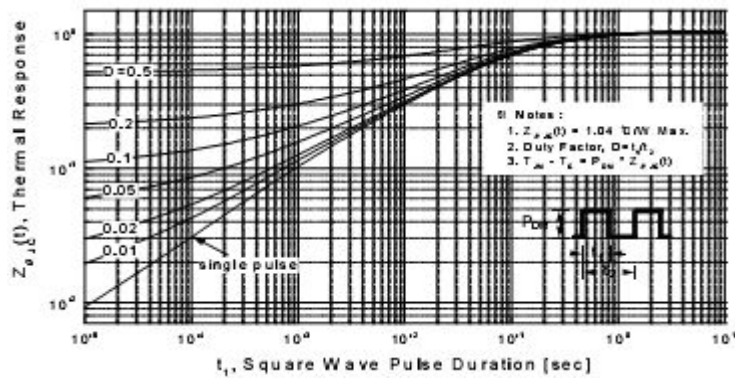
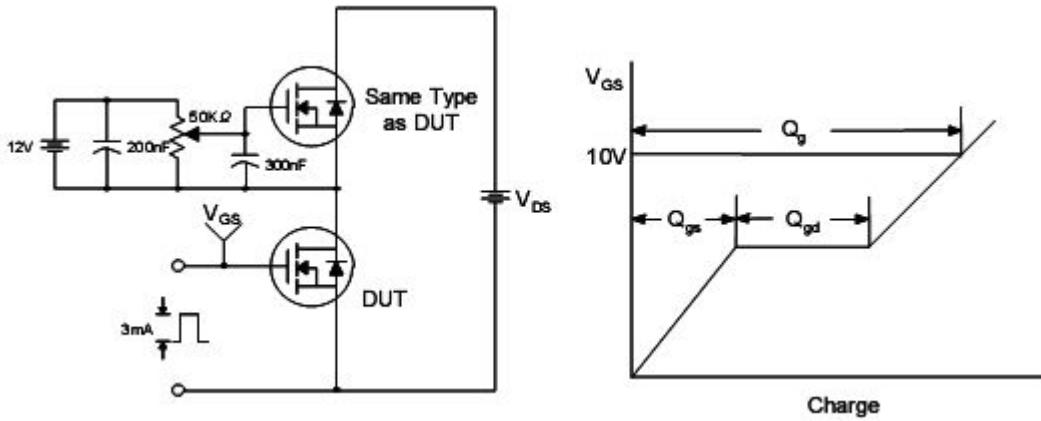


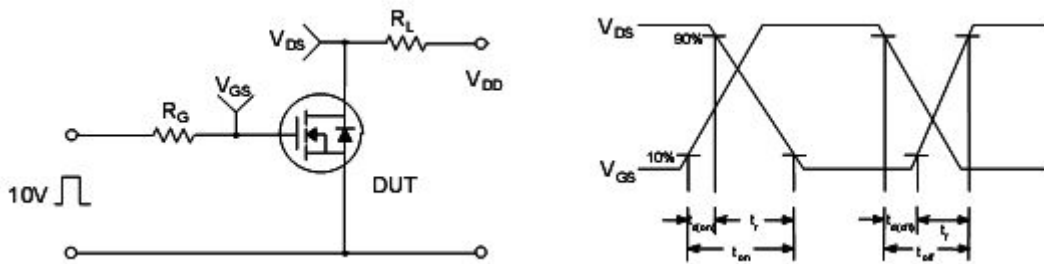
Figure11. Transient Thermal Response Curve

Test circuits and waveforms

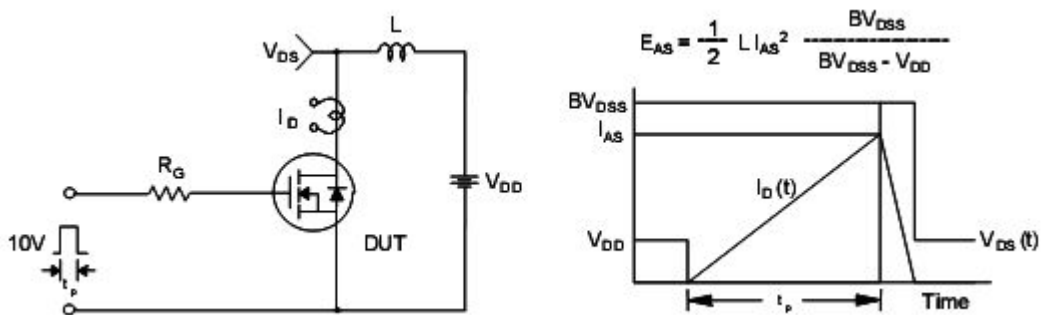
Gate Charge Test Circuit & Waveform



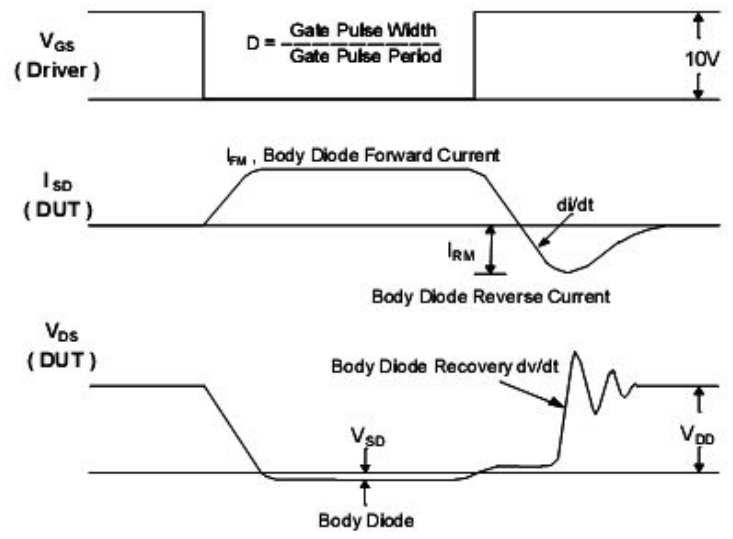
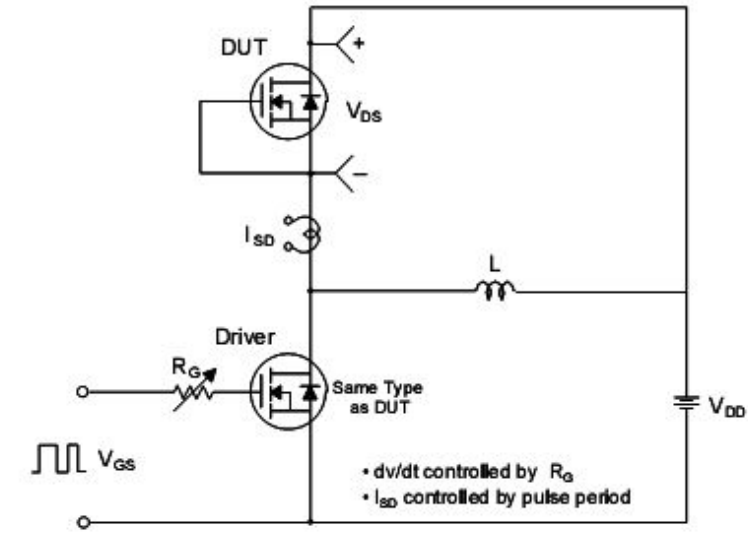
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Circuit & Waveforms



Mechanical Dimensions

