

TS12N60

600V N-Channel Mosfet

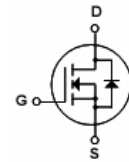
DRAWING

Features

- ◆ 12A,600V, $R_{DS(on)}=0.7\Omega@V_{GS}=10V$
- ◆ Ultra low gate charge(typical 42nc)
- ◆ Low Reverse transfer capacitance (Crsstypical25PF)
- ◆ Fast switching capability
- ◆ 100%avalanche energy specified
- ◆ Improved dv/dt capability, high ruggedness



G D S



General Description

- ◆ Package:ITO-220AB
- ◆ This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply.

Absolute Maximum Ratings

| Symbol    | Parameter                                     | Spec        | Units      |
|-----------|---|-------------|------------|
| $V_{DSS}$ | Drain-Source Voltage                          | 600         | V          |
| $I_D$     | Drain Current -Continuous( $T_c=25^\circ C$ ) | 12          | A          |
| $I_{DM}$  | Drain Current -Pulsed                         | 48          | A          |
| $V_{GSS}$ | Gate-Source Voltage                           | $\pm 30$    | V          |
| $E_{AS}$  | Single Pulsed Avalanche Energy                | 790         | mJ         |
| $I_{AR}$  | Avalanche Current                             | 12          | A          |
| $E_{AR}$  | Repetitive Avalanche Energy                   | 24          | mJ         |
| dv/dt     | Peak Diode Recovery dv/dt                     | 4.5         | V/ns       |
| $T_j$     | Junction Temperature                          | +150        | $^\circ C$ |
| $T_{opr}$ | Operating Temperature Range                   | -55 to +150 | $^\circ C$ |
| $T_{stg}$ | Storage Temperature                           | -55 to +150 | $^\circ C$ |

Electrical Characteristics( $T_c=25^\circ C$  unless otherwise noted)

Off Characteristics

| Symbol         | Parameter                                 | Test Conditions                             | Min | Typ | Max  | Units         |
|----------------|---|---|-----|-----|------|---------------|
| $BV_{DSS}$     | Drain-Source Breakdown Voltage            | $V_{GS}=0V, I_D=250\mu A$                   | 600 | -   | -    | V             |
| $BV_{DSS}/T_j$ | Breakdown Voltage Temperature Coefficient | $I_D=250\mu A$ , Referenced to $25^\circ C$ | -   | 0.7 | -    | V/ $^\circ C$ |
| $I_{DSS}$      | Zero Gate Voltage Drain Current           | $V_{DS}=600V, V_{GS}=0V$                    | -   | -   | 1    | $\mu A$       |
|                |   | $V_{DS}=480V, T_c=125^\circ C$              | -   | -   | 100  | $\mu A$       |
| $I_{GSSF}$     | Gate-Body Leakage Current, Forward        | $V_{GS}=30V, V_{DS}=0V$                     | -   | -   | 100  | nA            |
| $I_{GSSR}$     | Gate-Body Leakage Current, Reverse        | $V_{GS}=-30V, V_{DS}=0V$                    | -   | -   | -100 | nA            |

**On Characteristics**

| Symbol              | Parameter                         | Test Conditions  | Min | Typ  | Max | Units |
|---------------------|-----------------------------------|--|-----|------|-----|-------|
| V <sub>GSTH</sub>   | Gate Threshold Voltage            | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA | 2.0 | —    | 4.0 | V     |
| R <sub>DS(on)</sub> | Static Drain-Source On-Resistance | V <sub>GS</sub> =10V, I <sub>D</sub> =6A                 | —   | 0.61 | 0.7 | Ω     |

**Dynamic Characteristics**

| Symbol           | Parameter                    | Test Conditions                                     | Min | Typ  | Max  | Units |
|------------------|------------------------------|---|-----|------|------|-------|
| C <sub>JSS</sub> | Input Capacitance            | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz | —   | 1480 | 1900 | pF    |
| C <sub>oss</sub> | Output Capacitance           |   | —   | 200  | 270  | pF    |
| C <sub>rss</sub> | Reverse Transfer Capacitance |   | —   | 25   | 35   | pF    |

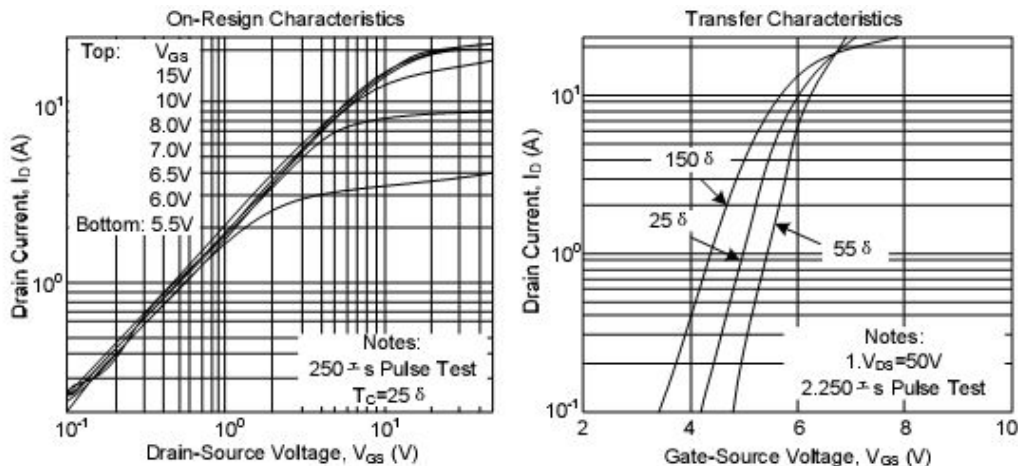
**Switching Characteristics**

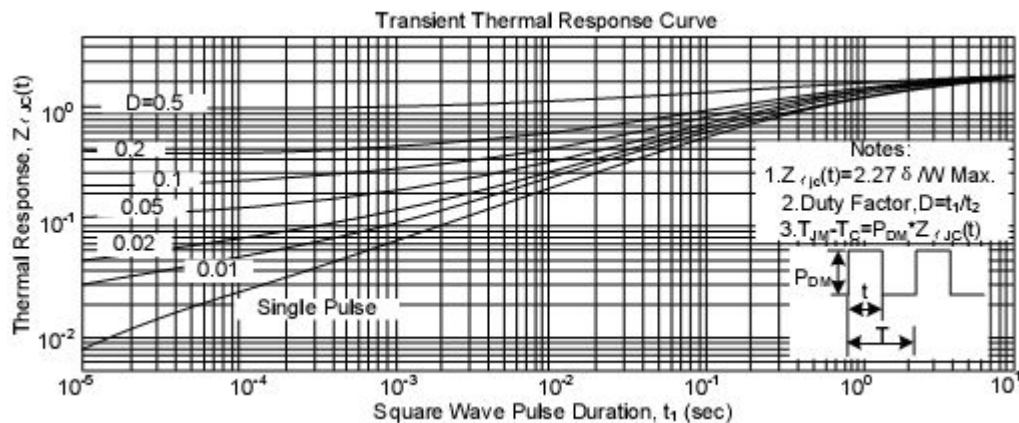
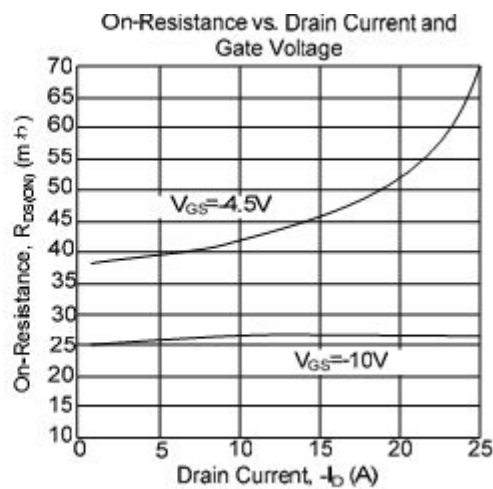
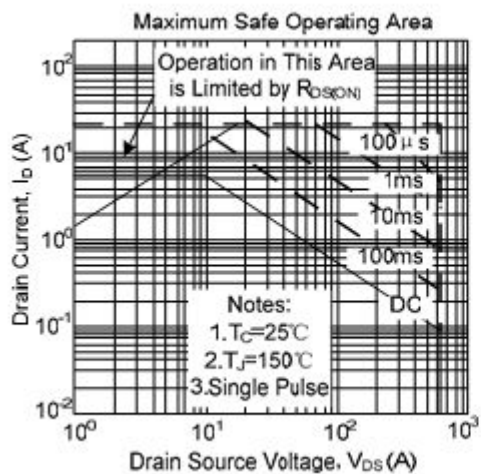
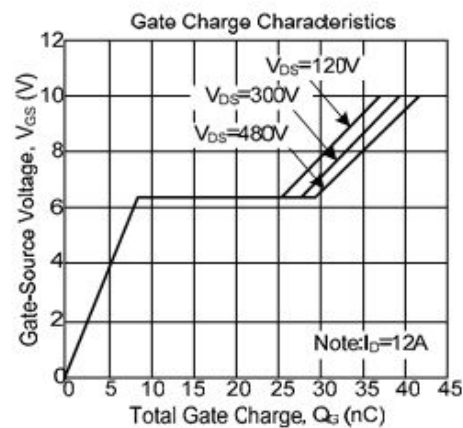
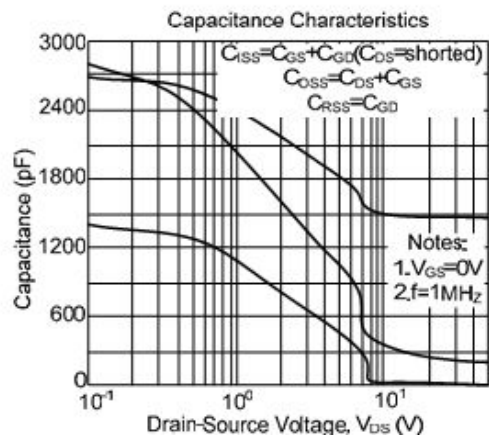
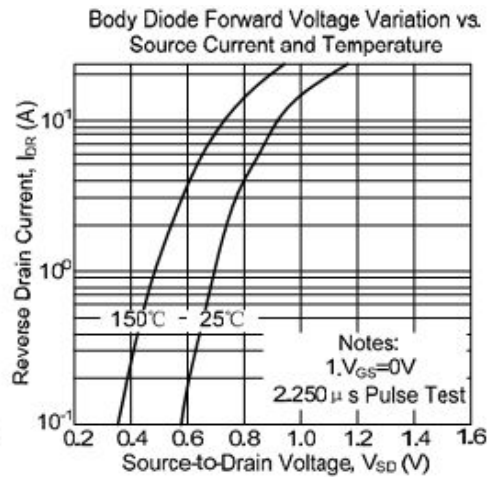
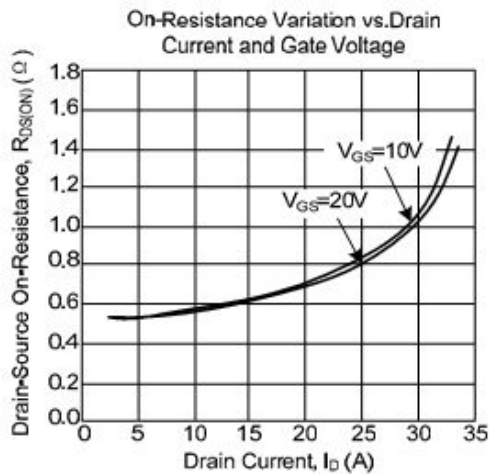
| Symbol            | Parameter           | Test Conditions  | Min | Typ | Max | Units |
|-------------------|---------------------|--|-----|-----|-----|-------|
| t <sub>don</sub>  | Turn-On Delay Time  | V <sub>DD</sub> =300V<br>I <sub>D</sub> =12A<br>R <sub>G</sub> =25Ω  | —   | 30  | 70  | ns    |
| t <sub>r</sub>    | Turn-On Rise Time   |  | —   | 115 | 240 | ns    |
| t <sub>doff</sub> | Turn-Off Delay Time |  | —   | 95  | 200 | ns    |
| t <sub>f</sub>    | Turn-Off Fall Time  | V <sub>DS</sub> =480V<br>I <sub>D</sub> =12A<br>V <sub>GS</sub> =10V | —   | 85  | 180 | ns    |
| Q <sub>g</sub>    | Total Gate Charge   |  | —   | 42  | 54  | nc    |
| Q <sub>gs</sub>   | Gate-Source Charge  |  | —   | 8.6 | —   | nc    |
| Q <sub>gd</sub>   | Gate-Drain Charge   | —  | 21  | —   | nc  |       |

**Drain-Source Diode Characteristics and Maximum Ratings**

| Symbol          | Parameter   | Test Conditions                           | Min | Typ | Max | Units |
|-----------------|---|---|-----|-----|-----|-------|
| I <sub>s</sub>  | Maximum Continuous Drain-source diode forward current |   | —   | —   | 12  | A     |
| I <sub>sm</sub> | Maximum pulsed drain-source diode forward current     |   | —   | —   | 48  | A     |
| V <sub>sd</sub> | Drain-source diode forward Voltage                    | V <sub>GS</sub> =0V, I <sub>S</sub> =6.2A | —   | —   | 1.4 | V     |
| T <sub>rr</sub> | Reverse Recovery Time                                 | V <sub>GS</sub> =0V, I <sub>S</sub> =6.2A | —   | 380 | —   | ns    |
| Q <sub>rr</sub> | Reverse Recovery charge                               | dif/dt=100A/us                            | —   | 3.5 | —   | uc    |

**Typical Characteristics**





Test circuits and waveforms

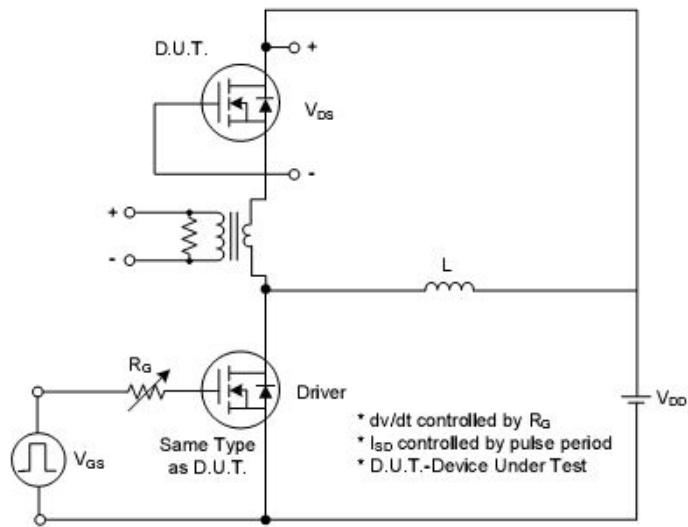


Fig. 1A Peak Diode Recovery  $dv/dt$  Test Circuit

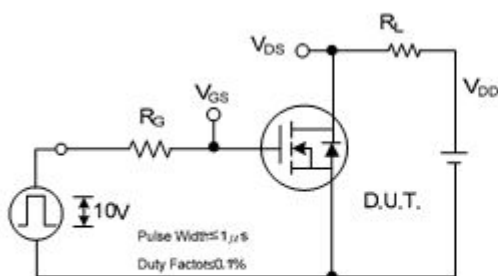
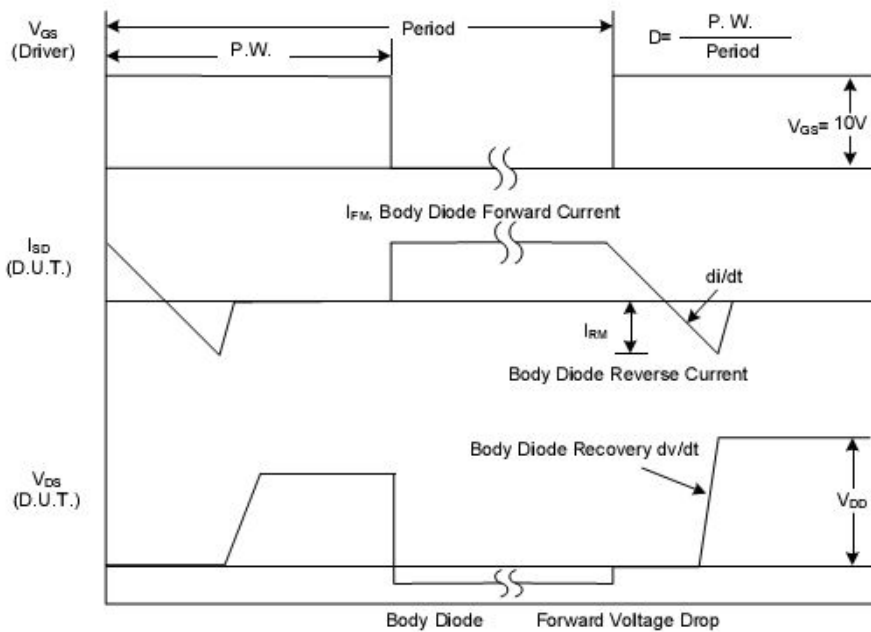


Fig. 2A Switching Test Circuit

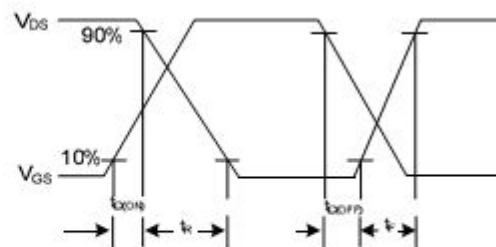


Fig. 2B Switching Waveforms

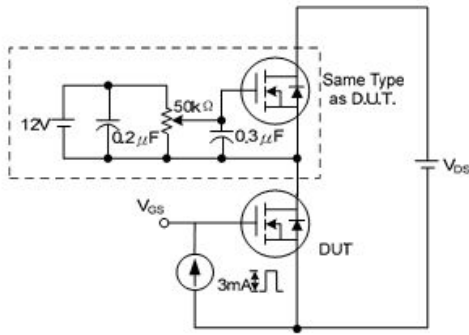


Fig. 3A Gate Charge Test Circuit

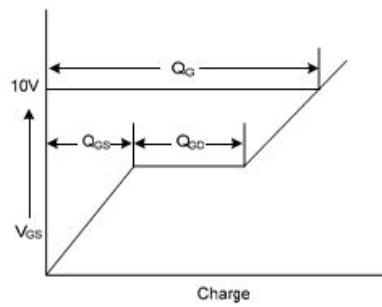


Fig. 3B Gate Charge Waveform

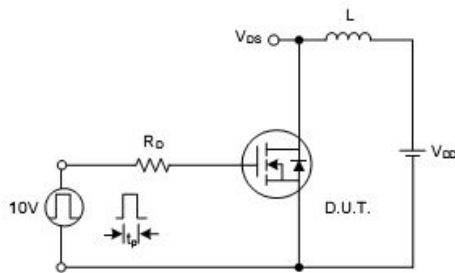


Fig. 4A Unclamped Inductive Switching Test Circuit

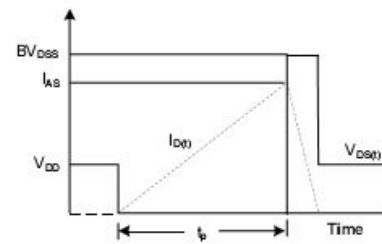


Fig. 4B Unclamped Inductive Switching Waveforms

Mechanical Dimensions

