

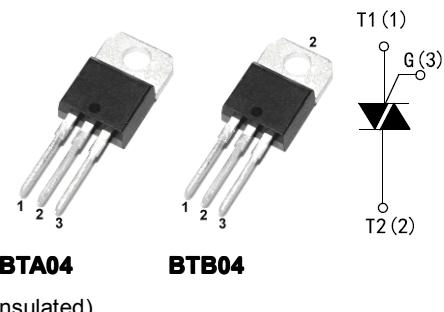
## **BTA04 / BTB04**

### **4A TRIACS**

#### **General Description**

- Package: TO-220T
- High current density due to double mesa technology, SIPOS and Glass passivation . BTA04/ BTB04 series triacs is Suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits... or phase control operation light dimmers, motor speed controllers.
- BTA04/ BTB04 series are 3 Quadrants triacs, They are specially recommended for use on inductive loads.

#### DRAWING



#### **Absolute Maximum Ratings**

| Parameter  | Symbol                           | Value        | Unit             |
|--|----------------------------------|--------------|------------------|
| Storage junction temperature range   | T <sub>stg</sub>                 | -40 to +150  | °C               |
| Operating junction temperature range   | T <sub>j</sub>                   | -40 to + 125 | °C               |
| Repetitive Peak OFF-state Voltage  | V <sub>DRM</sub>                 | 800          | V                |
| Repetitive Peak Reverse Voltage  | V <sub>RRM</sub>                 | 800          | V                |
| Non repetitive surge peak off-state voltage  | V <sub>DSM</sub>                 | 800          | V                |
| Non repetitive peak reverse voltage  | V <sub>RSM</sub>                 | 800          | V                |
| RMS on-state current(full sine wave)   | I <sub>T(RMS)</sub>              | 4            | A                |
| Non repetitive surge peak on-state current(full cycle,T <sub>j</sub> =25°C)  | f=60Hz,t=16.7ms<br>f=50Hz,t=20ms | 27<br>25     | A                |
| I <sup>2</sup> t Value for fusing  | I <sup>2</sup> t                 | 3.1          | A <sup>2</sup> s |
| Critical rate of rise of on-state current I <sub>G</sub> =2*I <sub>GT</sub> ,t <sub>r</sub> ≤100ns,f=120Hz,T <sub>j</sub> =125°C | I <sub>V</sub><br>dI/dt          | 50<br>20     | A/us             |
| Peak gate current(tp=20us,T <sub>j</sub> =125°C)   | I <sub>GM</sub>                  | 2            | A                |
| Peak gate power dissipation(tp=20us,T <sub>j</sub> =125°C)   | P <sub>GM</sub>                  | 5            | W                |
| Average gate power dissipation(T <sub>j</sub> =125°C)  | P <sub>G(AV)</sub>               | 0.5          | W                |

**Electrical Characteristics** ( $T_j=25^\circ\text{C}$ , unless otherwise specified)

| <b>Symbol</b> | <b>Test Condition</b>  | <b>Quadrant</b> |     | <b>Limit</b> |          |          |          | <b>Unit</b> |
|---------------|--|-----------------|-----|--------------|----------|----------|----------|-------------|
|               |  |                 |     | <b>D</b>     | <b>E</b> | <b>F</b> | <b>G</b> |             |
| $I_{GT}$      | $V_D=12\text{V}, R_L=33\Omega$                                   | I - II - III IV | MAX | 5            | 10       | 25       | 50       | mA          |
| $V_{GT}$      |  |                 |     | 10           | 25       | 70       | 100      |             |
| $V_{GD}$      | $V_D=V_{DRM}$ $R_L=3.3\text{K}\Omega$<br>$T_j=125^\circ\text{C}$ | ALL             | MIN | 0.2          |          |          |          | V           |
| $I_L$         | $I_G=1.2I_{GT}$  | I - III - IV    | MAX | 15           | 30       | 40       | 60       | mA          |
|               |  | II              | MAX | 20           | 40       | 60       | 90       | mA          |
| $I_H$         | $I_T=100\text{mA}$   |                 | MAX | 10           | 25       | 30       | 60       | mA          |
| $Dv/dt$       | $VD=67\%V_{DRM}$ gate open $T_j=125^\circ\text{C}$               |                 | MIN | 5            | 10       | 50       | 200      | V/us        |
| $(Dv/dt)_c$   | $(dl/dt)_c=1.1\text{A/ms}$ $T_j=125^\circ\text{C}$               |                 | MIN | 1            | 2        | 5        | 10       | V/us        |

**Static Characteristics**

| <b>Symbol</b> | <b>Parameter</b>     | <b>Value</b> | <b>Unit</b> |
|---------------|----------------------|--------------|-------------|
| $R_{th}(J-C)$ | Junction to case(AC) | 3.7          | °C/W        |

**Thermal Resistances**

| <b>Symbol</b> | <b>Parameter</b>                   | <b>Value(MAX)</b>       | <b>Unit</b> |    |
|---------------|------------------------------------|-------------------------|-------------|----|
| $V_{TM}$      | $ITM=5\text{A}, tp=380\mu\text{s}$ | $T_j=25^\circ\text{C}$  | 1.7         | V  |
| $I_{DRM}$     | $VD=V_{DRM}$ $VR=V_{RRM}$          | $T_j=25^\circ\text{C}$  | 5           | uA |
| $I_{RRM}$     |                                    | $T_j=125^\circ\text{C}$ | 1           | mA |

**Typical Characteristics**

FIG.1:Maximum power dissipation versus RMS on-state current(full cycle)

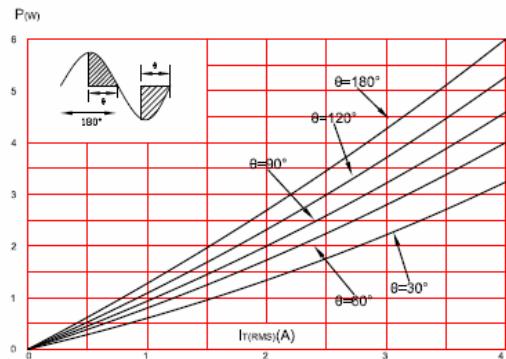


FIG.2:RMS on-state current versus case temperature(full cycle)

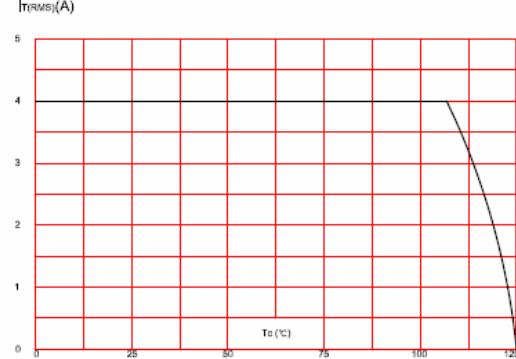


FIG.3:On-state characteristics (maximum values).

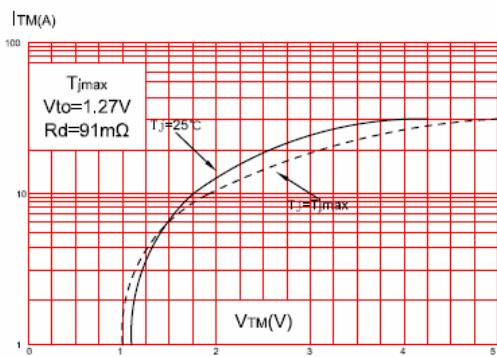


FIG.5:Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$ , and corresponding value of  $I^2t$ .

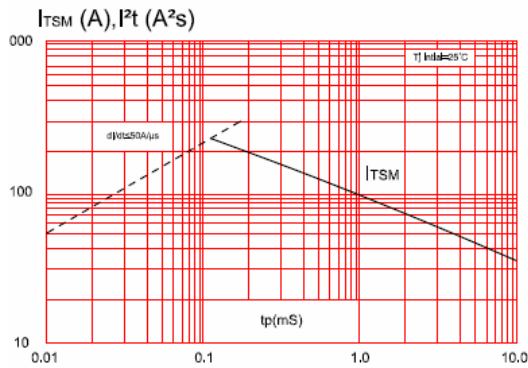


FIG.4:Surge peak on-state current versus number of cycles.

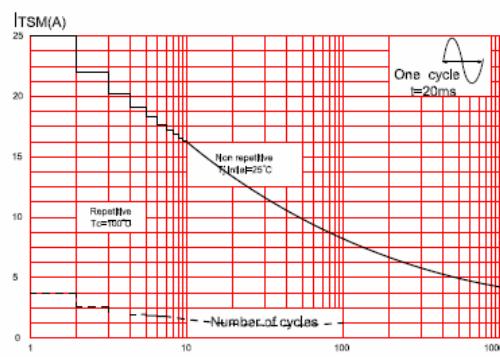
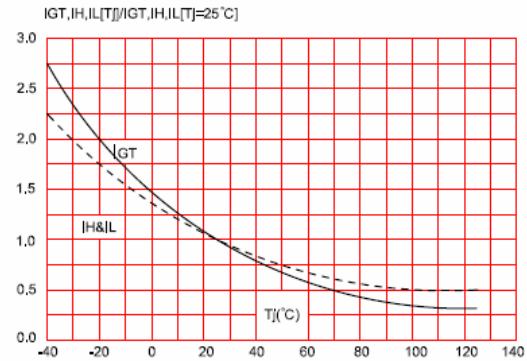


FIG.6:Relative variations of gate trigger current,holding current and latching current versus junction temperature(typical values)



## **Package Mechanical Data**

