

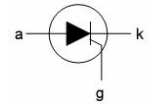
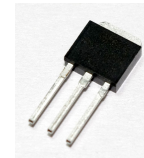
BT152

Thyristors

DRAWING

General Description

- ◆ Package: TO-251
- ◆ Glass passivated thyristors in a plastic envelope, Intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.



K A G

Limiting Values

Limiting values in accordance with the absolute Maximum System(IEC134)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{DRM}, V_{RRM}	Repetitive peak off-state Voltages			600	V
$I_{T(AV)}$	Average on-state current	half sine wave; $T_{mb} \leq 109^{\circ}C$		13	A
$I_{T(RMS)}$	RMS on-state current	all conduction angles		20	A
I_{TSM}	Non-repetitive peak on-state current	half sine wave; $T_j = 25^{\circ}C$ prior to surge			
		$t = 10ms$		200	A
		$t = 8.3ms$		220	A
I^2T	I^2T for fusing	$t = 10ms$		200	A^2S
D_{it}/dt	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 20A; I_G = 50mA; D_{ig}/dt = 50mA/us$		200	A/us
I_{GM}	Peak gate current			5	A
V_{GM}	Peak gate voltage			5	V
V_{RGM}	Peak reverse gate voltage			5	V
P_{GM}	Peak gate power			20	W
$P_{G(AV)}$	Average gate power	over any 20 ms period		0.5	W
T_{STG}	Storage temperature		-40	150	$^{\circ}C$
T_j	Operating junction temperature			125	$^{\circ}C$

Thermal Resistances

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
R_{thj-mb}	Thermal resistance junction to mounting base				1.8	K/W
R_{thj-a}	Thermal resistance junction to ambient			75		K/W

Static Characteristics(T_J=25°C unless otherwise stated)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
I _{GT}	Gate trigger current	V _D =12V; I _T =0.1A		3	32	mA
I _L	Latching current	V _D =12V; I _{GT} =0.1A		25	80	mA
I _H	Holding current	V _D =12V; I _{GT} =0.1A		15	60	mA
V _T	On-state voltage	I _T =23A		1.4	1.75	V
V _{GT}	Gate trigger voltage	V _D =12V; I _T =0.1A		0.6	1.5	V
		V _D =V _{DRM(MAX)} ; I _T =0.1A; T _J =125°C	0.25	0.4		V
I _{D,IR}	Off-state leakage current	V _D =V _{DRM(MAX)} ; V _R =V _{RRM(MAX)} ; T _J =125°C		0.2	1.0	mA

Dynamic Characteristics(T_J=25°C unless otherwise stated)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
DVD/dt	Critical rate of rise or off-state voltage	V _{DM} =67%V _{DRM(MAX)} ; T _J =125°C; exponential waveform Gate open circuit	200	300		V/us
t _{gt}	Gate controlled turn-on time	I _{TM} =40A; V _D =V _{DRM(MAX)} ; I _G =0.1A Dig/dt=5A/us		2		us
t _q	Circuit commutated turn-off time	V _D =67%V _{DRM(MAX)} ; T _J =125°C		70		us
		I _{TM} =20A; V _R =25V; D _{itm} /dt=30A/us				
		dvd/dt=50V/us; R _{gk} =100Ω				

Typical Characteristics

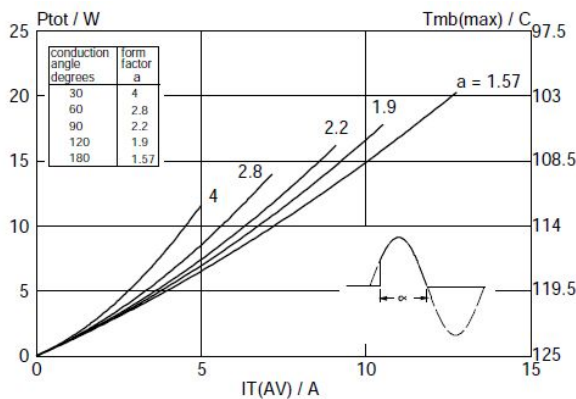


Fig.1. Maximum on-state dissipation, P_{tot}, versus average on-state current, I_{T(AV)}, where a = form factor = I_{T(RMS)}/I_{T(AV)}.

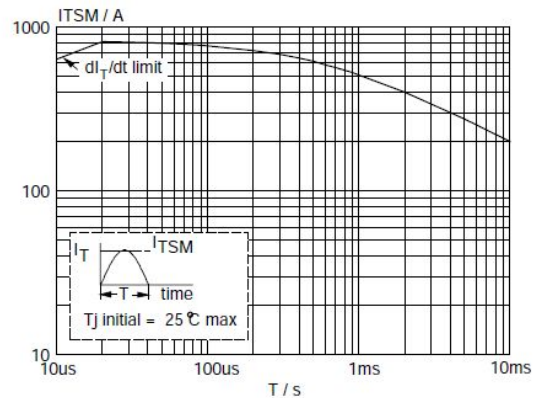


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} versus pulse width t_p for sinusoidal currents, t_p ≤ 10ms.

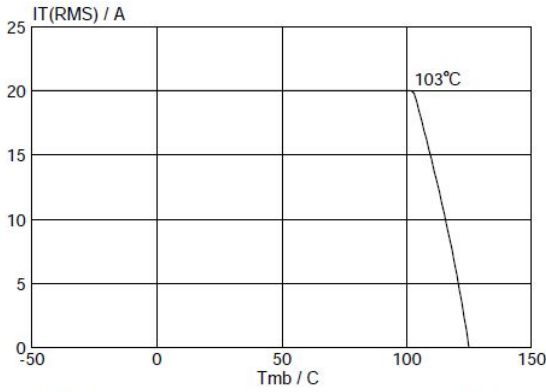


Fig.3. Maximum permissible rms current $I_{T(RMS)}$ versus mounting base temperature T_{mb} .

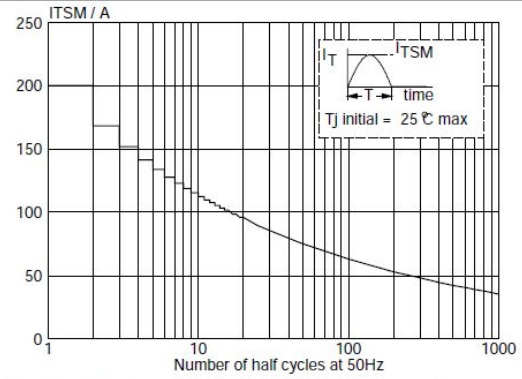


Fig.4. Maximum permissible non-repetitive peak on-state current I_{TSMr} versus number of cycles, for sinusoidal currents, $f = 50$ Hz.

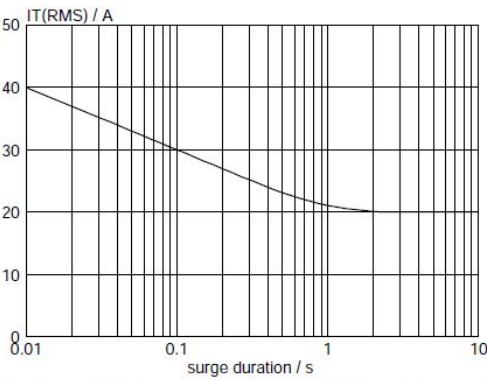


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$ versus surge duration, for sinusoidal currents, $f = 50$ Hz; $T_{mb} \leq 103^\circ\text{C}$.

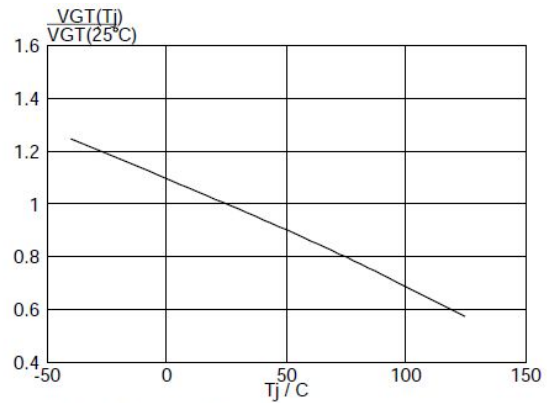


Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^\circ\text{C})$, versus junction temperature T_j .

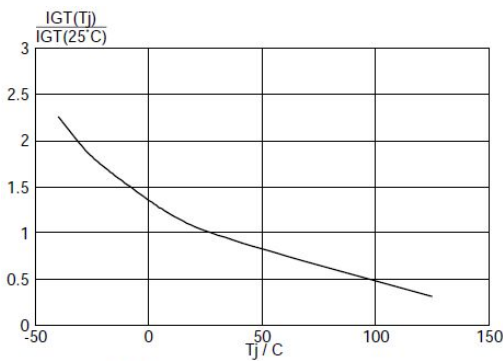


Fig.7. Normalised gate trigger current $I_{GT}(T_j)/I_{GT}(25^\circ\text{C})$, versus junction temperature T_j .

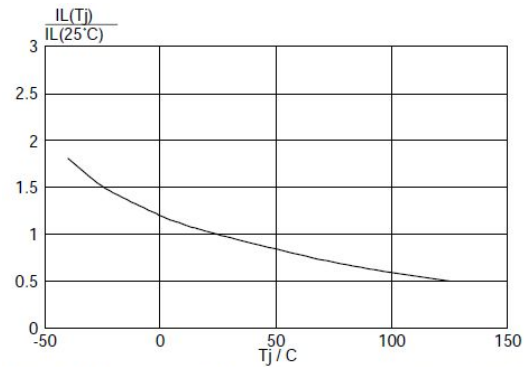


Fig.8. Normalised latching current $I_L(T_j)/I_L(25^\circ\text{C})$, versus junction temperature T_j .

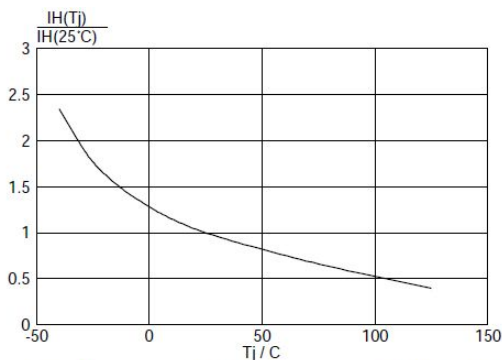


Fig.9. Normalised holding current $I_H(T_j)/I_H(25^\circ\text{C})$, versus junction temperature T_j .

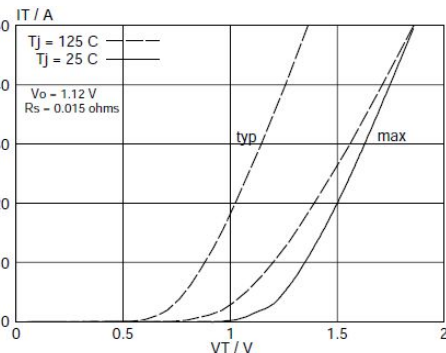


Fig.10. Typical and maximum on-state characteristic.

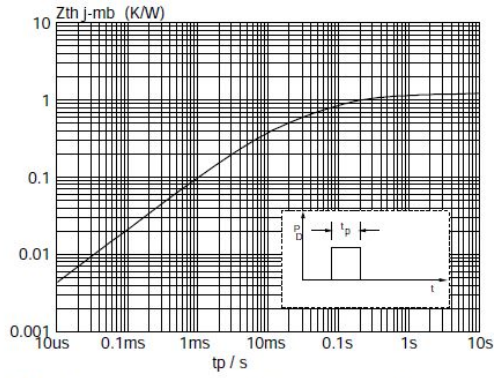


Fig.11. Transient thermal impedance Z_{thj-mb} versus pulse width t_p .

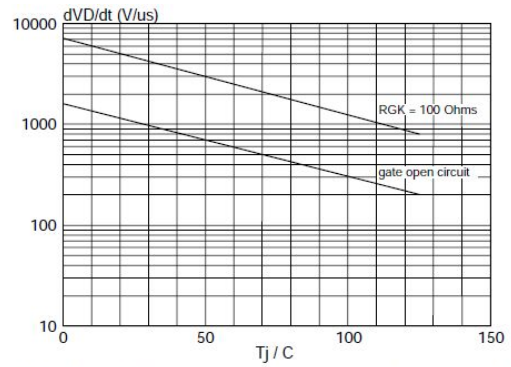


Fig.12. Typical, critical rate of rise of off-state voltage, dV_D/dt versus junction temperature T_J .

Mechanical Dimensions

